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Optimum power/performance pipeline depth - group of 7 »

A Hartstein, TR Puzak - Microarchitecture, 2003. MICRO-36. Proceedings. 36th Annual ..., 2003 - [ieeexplore.ieee.org](#)

... using IPC degradation factors for adding **cycles** to critical ... **power** be just below some **maximum** value, which ... detailed comparisons of theory and **simulation** for all ...
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Advances in modeling and simulation of vacuum electronic devices - group of 4 »

TM Antonsen Jr, AA Mondelli, B Levush, JP ... - Proceedings of the IEEE, 1999 - [ieeexplore.ieee.org](#)

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A Hartstein, TR Puzak - ACM Transactions on Architecture and Code Optimization (TACO) ..., 2004 - [portal.acm.org](#)

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... driven **simulation** of the Spec95 integer benchmark suite will be ... be mounted on a printed

circuit board configured ... are attempting to exploit the **maximum**, or **peak** ...

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JLA Rodrigo - [lsi.die.upm.es](#)

... the registers while reducing the **power** consumption to a **minimum**. ... the general trend is for **maximum** processor **power** ... due to the lack of **simulation** information at ...

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A Cell-Sparing Electric Field Stimulation Technique for High-Throughput

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... Figure 3-7 **Peak** value of ultrasonic signal Specimen Sk. ... 35 Time in units of 200 **cycles** ...

demonstrated by **simulation** the benefits of an optimal open loop control ...

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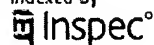
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Peak power tracking in parallel connected convertors

Siri, K. Caliskan, V.A. Lee, C.Q.
Electron. Res. Lab., Illinois Univ., Chicago, IL ;

This paper appears in: [Circuits, Devices and Systems, IEE Proceedings G](#)

Publication Date: Apr 1993

Volume: 140, Issue: 2

On page(s): 106-116

ISSN: 0956-3768

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INSPEC Accession Number: 4397694

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Abstract

A control scheme for parallel connected convertor systems, which will transfer the maximum average power from a nonideal voltage source, is presented. Monitoring the rates of change in both the average and average input power from the source, the proposed control method can dynamically regulate the convertor system to track the peak power point of the source. The amplitude and frequency of the limit cycle around the system peak power point is analysed. To improve the system reliability, the central limit distribution control is incorporated into the proposed scheme to uniformly supplied power among the parallel connected convertors

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IEEE JNL IEEE Journal or Magazine

IEE JNL IEE Journal or Magazine

IEEE CNF IEEE Conference Proceeding

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IEEE STD IEEE Standard

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